## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Chang Seo Park et al.

Docket No.:

061472-0308425

erial No.:

10/826,665

Group Art Unit:

2813

Filing Date:

April 16, 2004

Examiner:

Harrison, Monica D.

For:

A METHOD TO FABRICATE CMOS DEVICE WITH DUAL METAL

**GATE ELECTRODES** 

## **OFFICE ACTION RESPONSE**

MAIL STOP AMENDMENT Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

This paper is filed in response to the Office Action dated November 27, 2006.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.

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